

FIG. 1

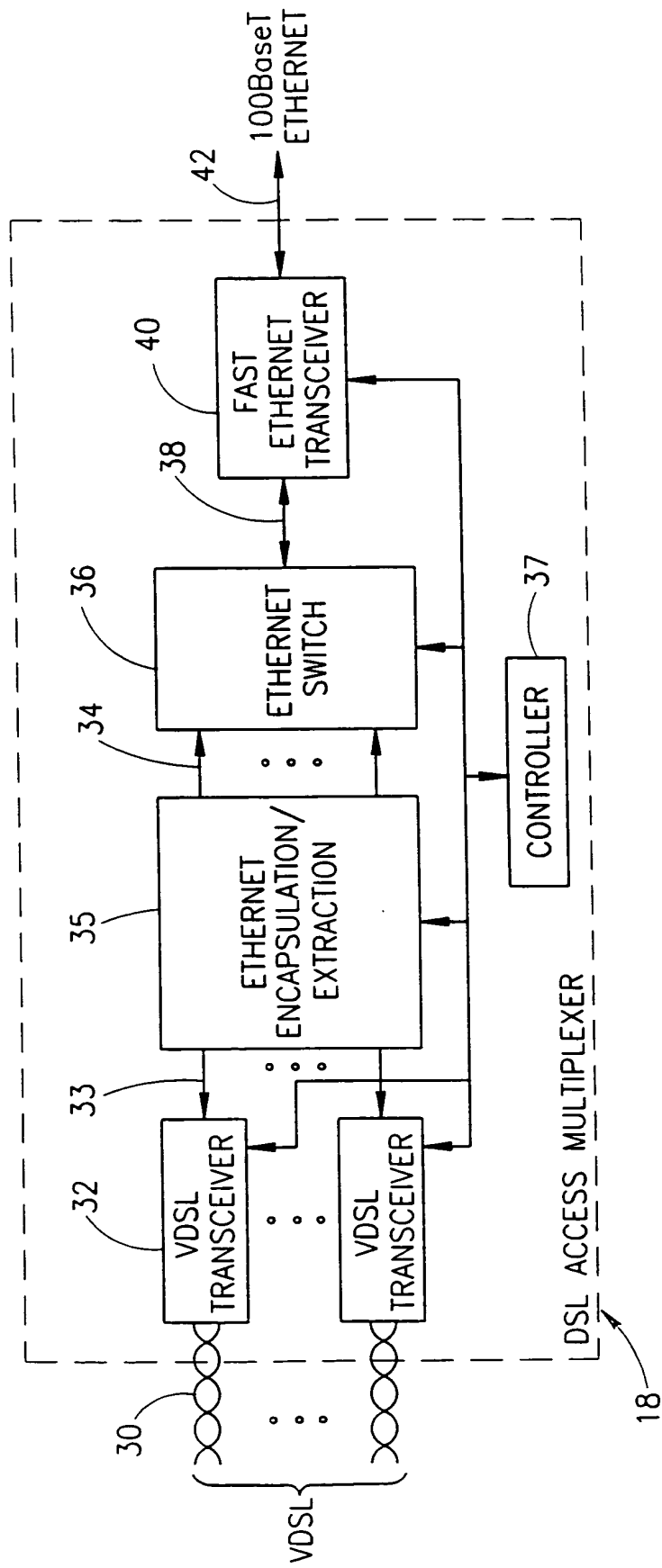


FIG. 2

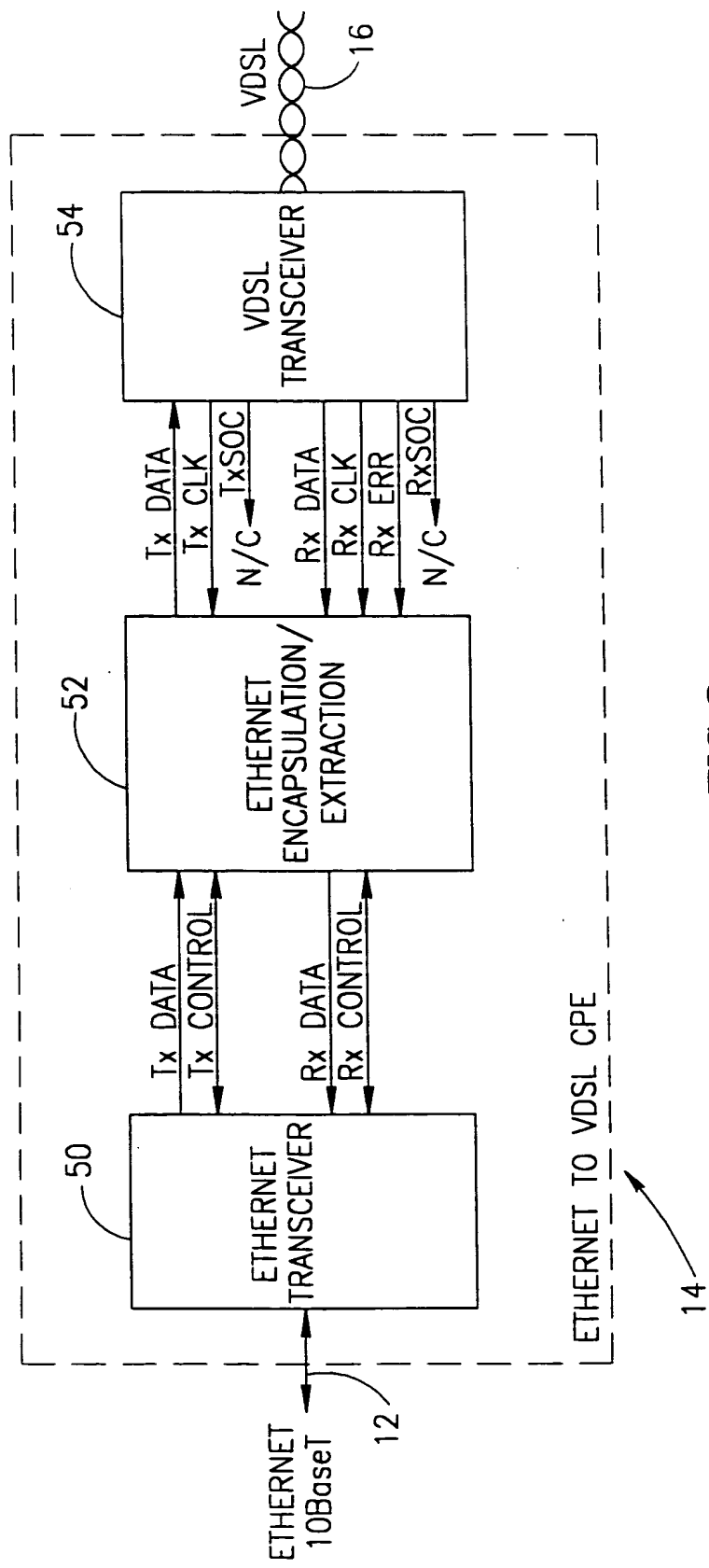


FIG.3

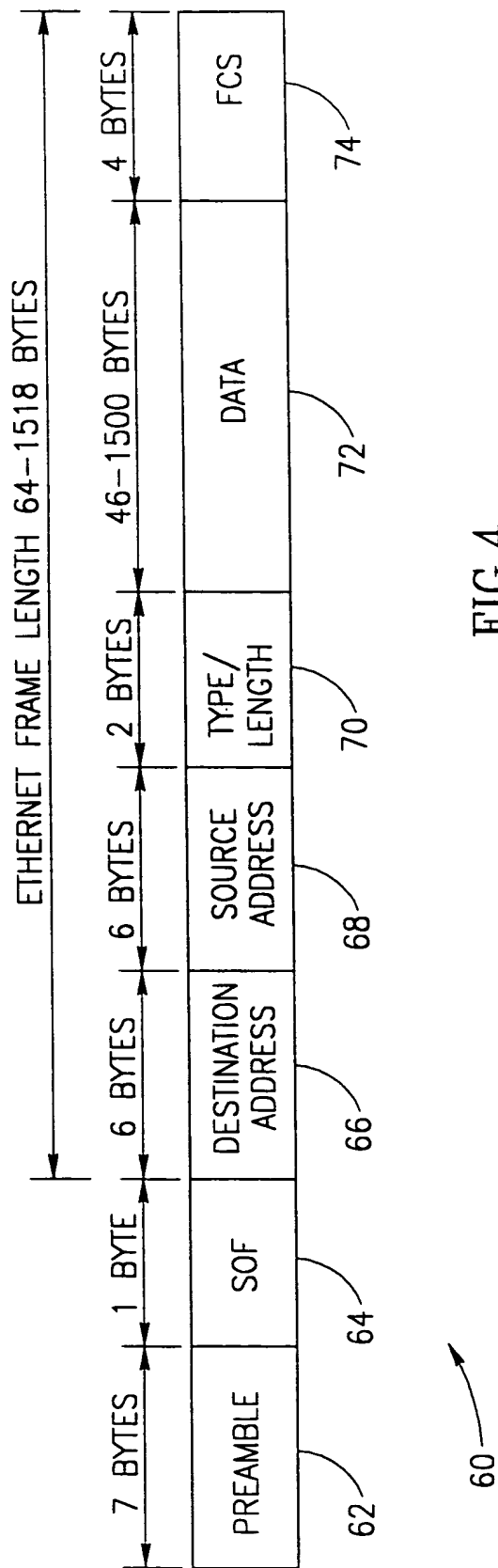


FIG.4

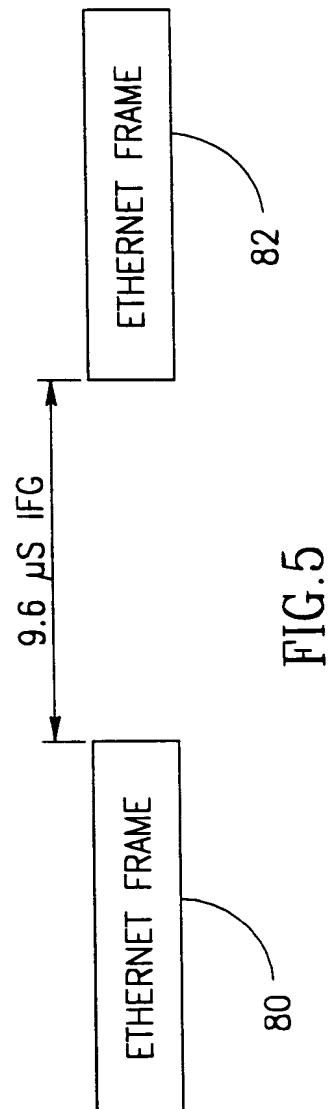


FIG.5

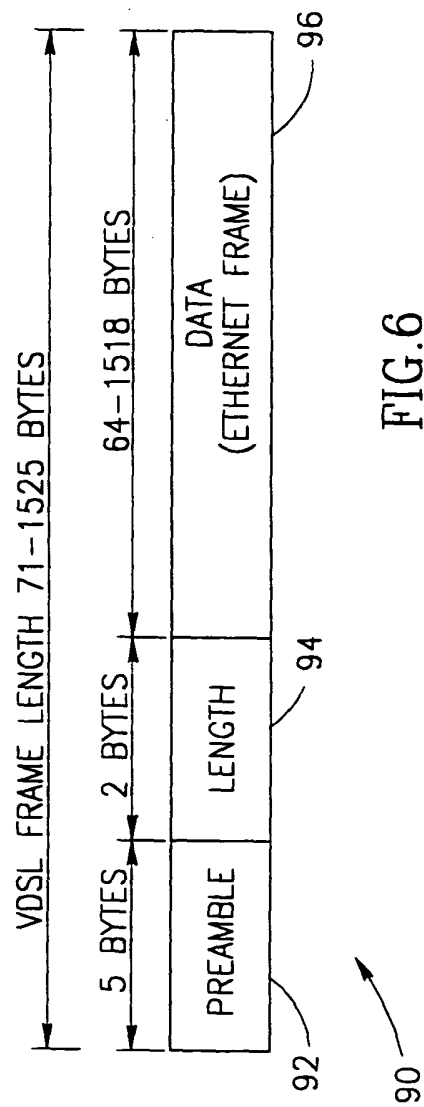


FIG. 6

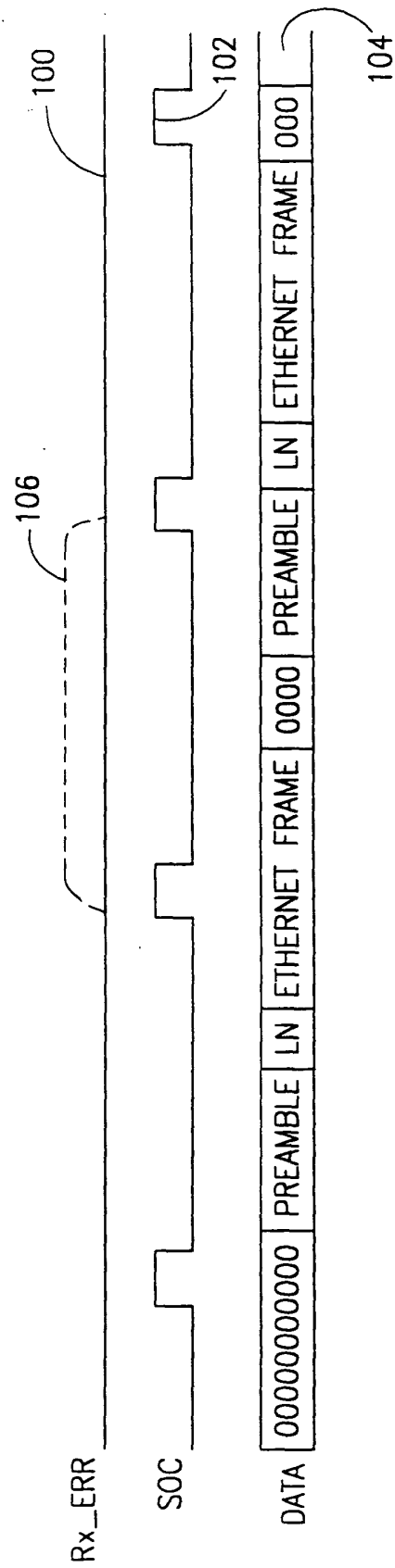


FIG. 7

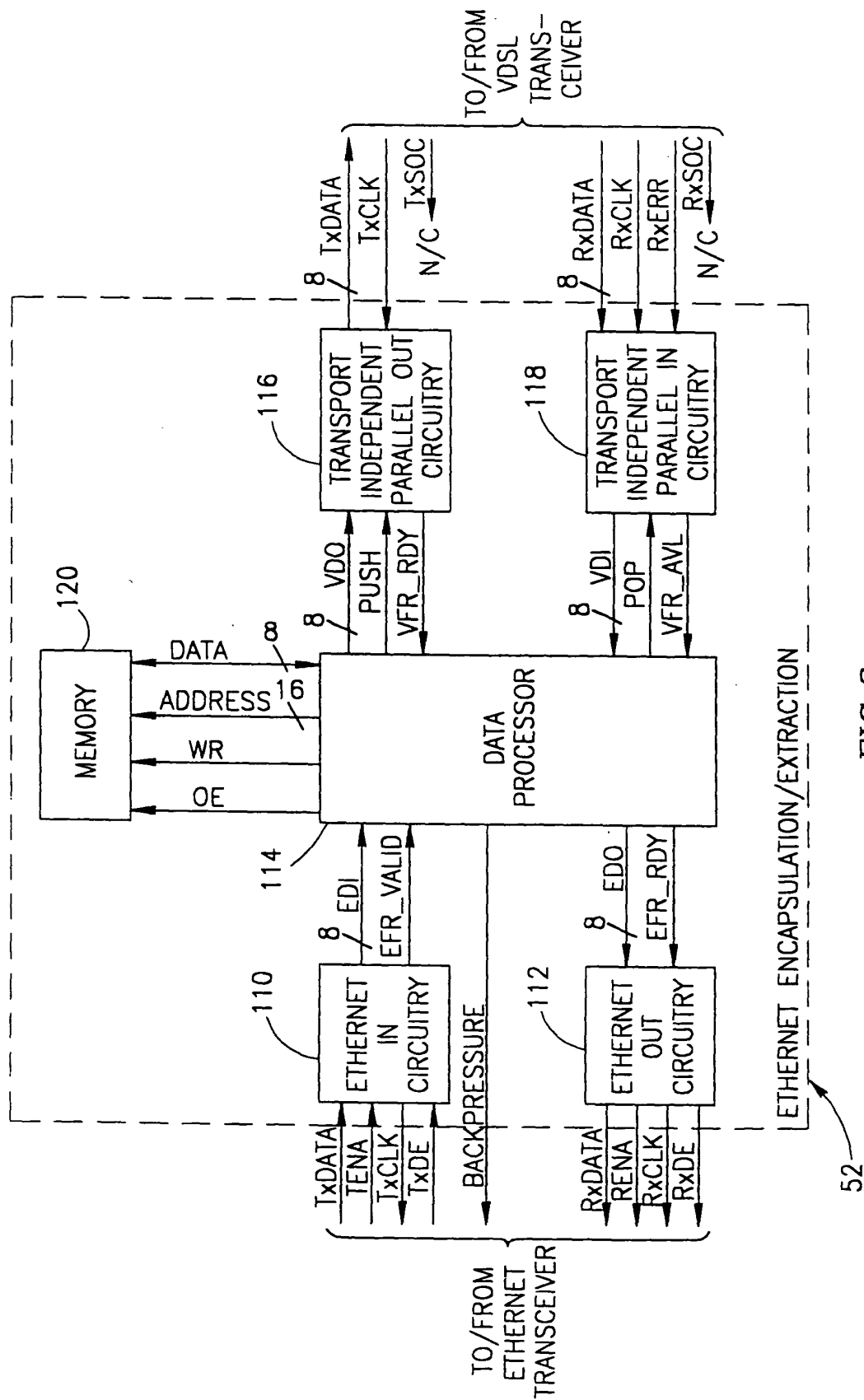


FIG.8

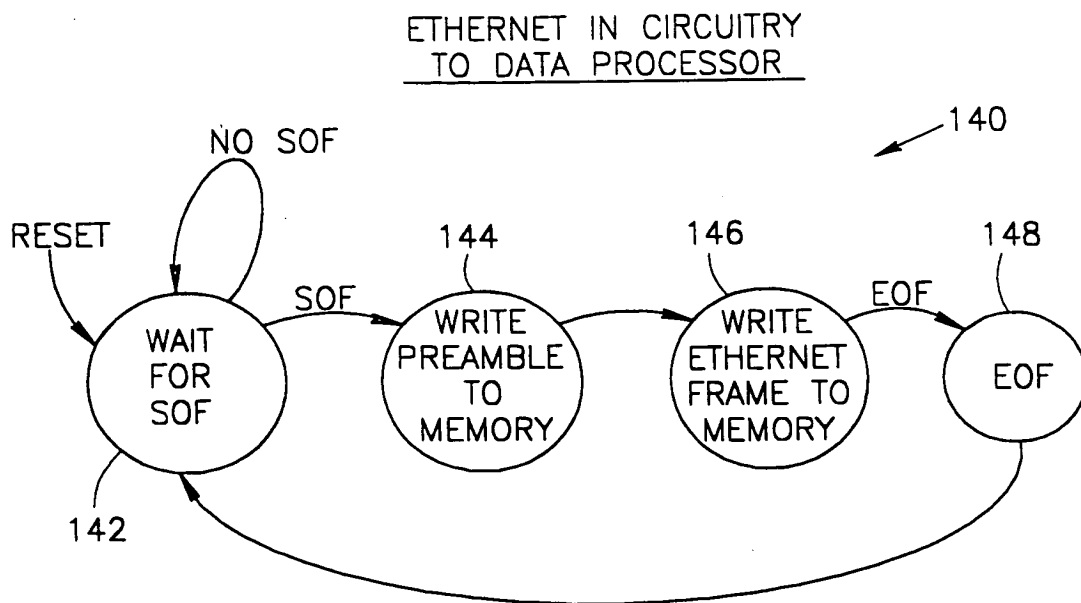


FIG.9

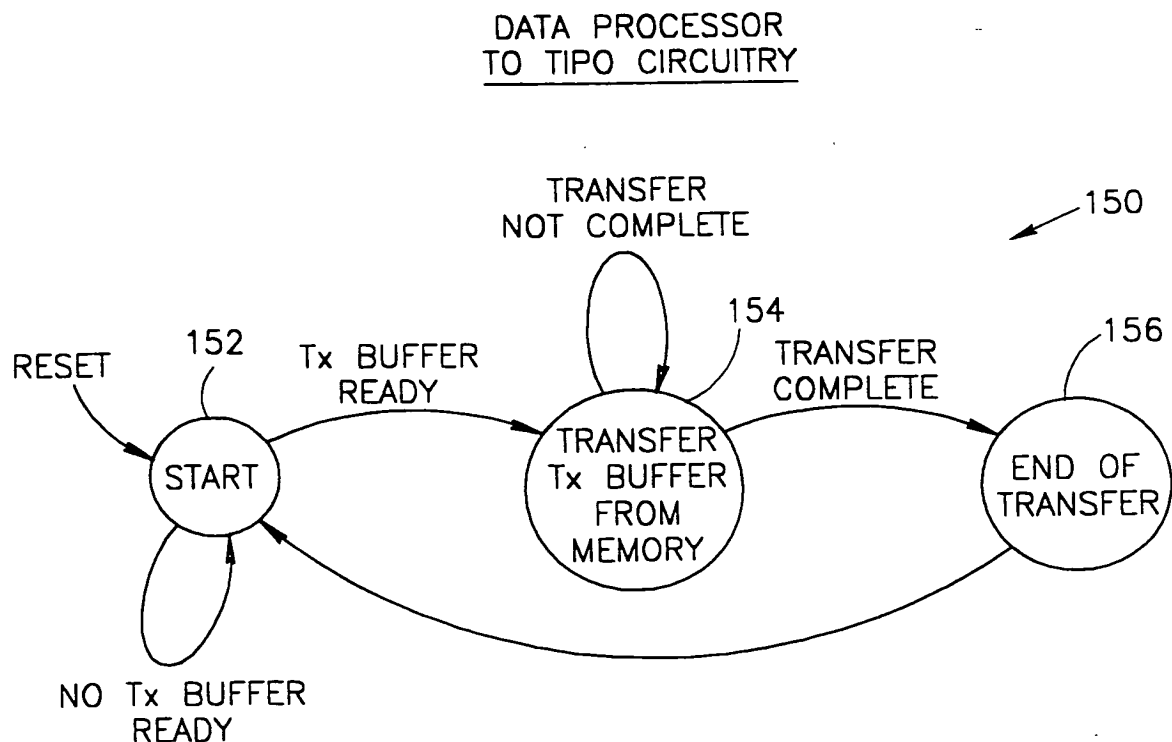


FIG.10

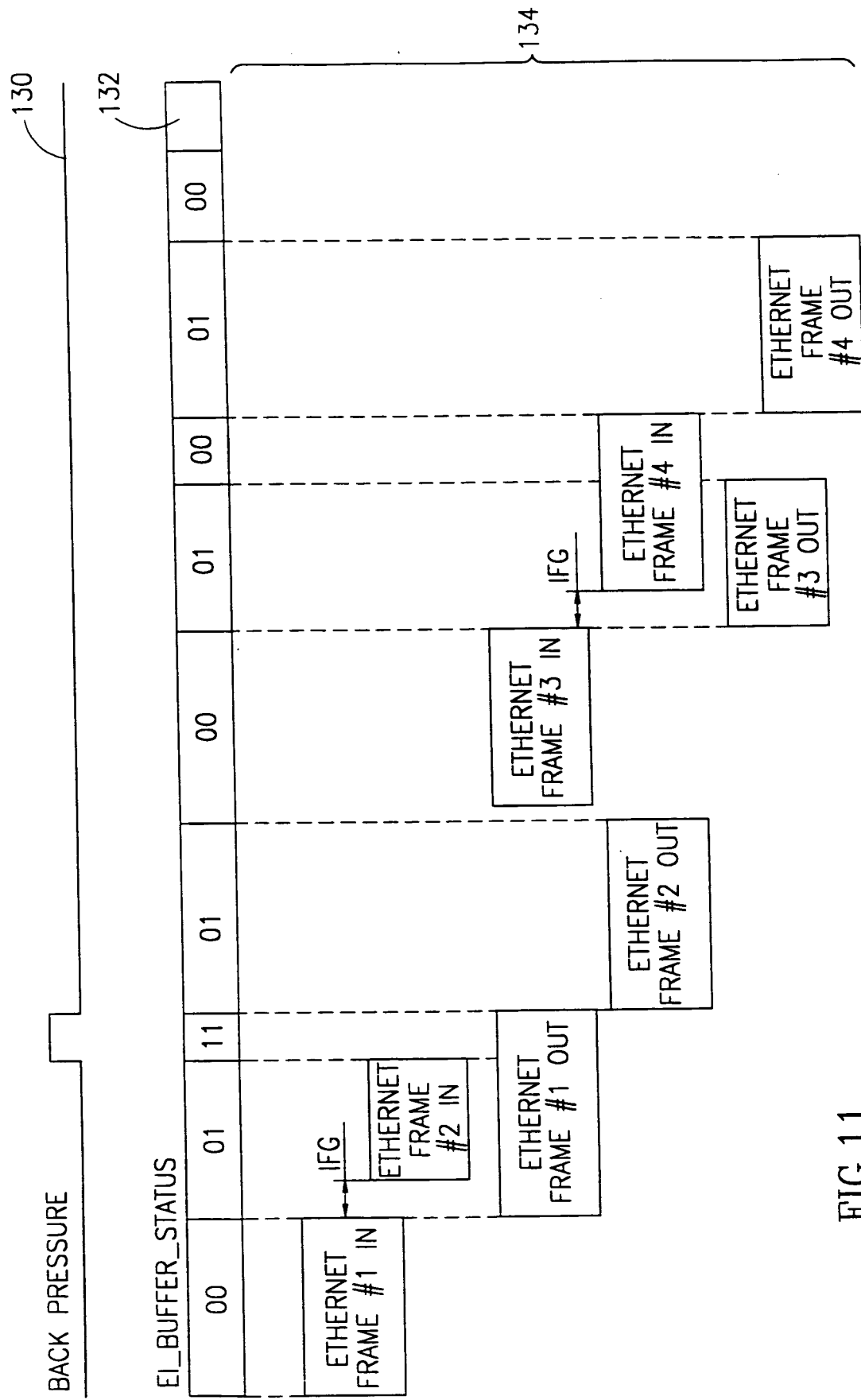


FIG.11

TIPI CIRCUITRY TO DATA PROCESSOR

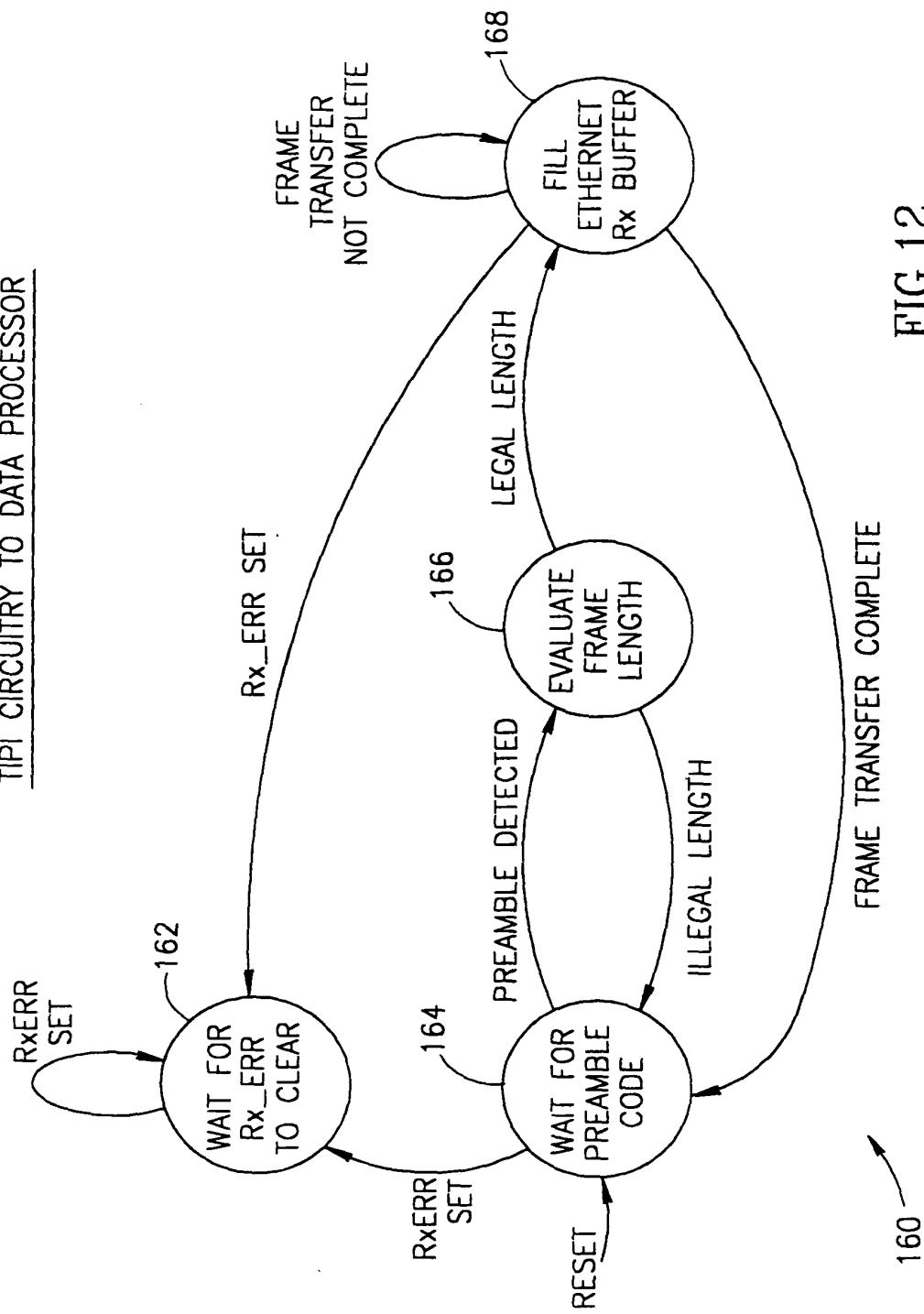


FIG.12



DATA PROCESSOR  
TO ETHERNET OUT CIRCUITRY

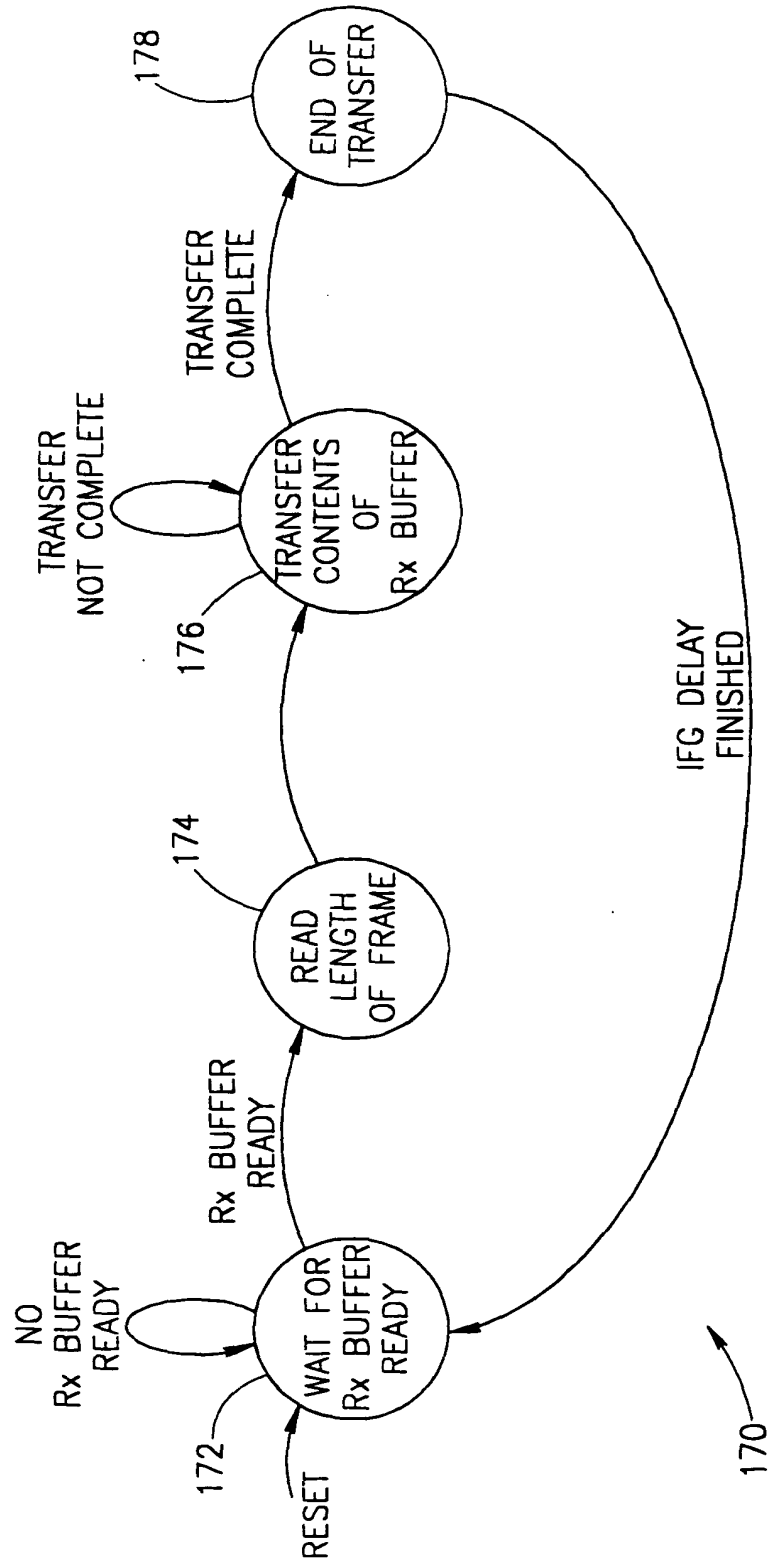


FIG.13